	Туре	L	#	Hits	Search Text	DBs	Time Stamp	Comment s
1	BRS	L1		44465 6	latch latching flip-flop\$1 flip adj flop\$1	USPA T; US-P GPUB ; EPO; JPO; DERW ENT	2004/09/2 1 07:37	
2	BRS	L2			<pre>1 and (latch latching flip-flop\$1 flip adj flop\$1) with (selecting selector selection select mux multiplexor multiplexer multiplexing)</pre>	USPA T; US-P GPUB ; EPO; JPO; DERW ENT	2004/09/2 1 07:38	
3	BRS	L3			2 and (latch latching flip-flop\$1 flip adj flop\$1) with (selecting selector selection select mux multiplexor multiplexer multiplexing) with (delay delayed delaying)	USPA T; US-P GPUB; EPO; JPO; DERW ENT	2004/09/2 1 07:41	
4	BRS	L4		3176	3 and control\$4	USPA T; US-P GPUB ; EPO; JPO; DERW ENT	2004/09/2 1 07:40	
5	BRS	L5		2697	4 and clock	USPA T; US-P GPUB; EPO; JPO; DERW ENT	2004/09/2 1 07:40	

	Туре	L #	Hits	Search Text	DBs	Time Stamp	Comment s
6	BRS	L6	1627	5 and (synchronization synchronized synchronizing synchronize)	USPA T; US-P GPUB; EPO; JPO; DERW ENT	2004/09/2 1 07:41	
7	BRS	L7	1065	6 and (latch latching flip-flop\$1 flip adj flop\$1) with (delay delayed delaying) with clock	USPA T; US-P GPUB; EPO; JPO; DERW ENT	2004/09/2 1 07:44	
8	BRS	L8	221	7 and (latch latching flip-flop\$1 flip adj flop\$1) with (delay delayed delaying) with clock with (synchronization synchronized synchronizing synchronize)	USPA T; US-P GPUB; EPO; JPO; DERW ENT	2004/09/2 1 07:44	